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Applicant(s): HSU et al.

Examiner: Chambliss, Alonzo

Serial No.: 09/769,080

Group Art Unit: 2827

Filed: January 25, 2001

Docket: 728-189 (YOR9-2000-0756)

Dated: August 23, 2002

For: **METHOD AND APPARATUS FOR
GLOBALLY ALIGNING THE FRONT
AND BACK SIDES OF A SUBSTRATE**

Box Fee
Commissioner for Patents
Washington, D.C. 20231

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated May 23, 2002, please consider the following amendments and remarks.

IN THE DRAWINGS

A separate drawing page having corrected Figure 4 is attached. Reference number 28 now points to the first circuit system. Acceptance of corrected Figure 4 is requested.

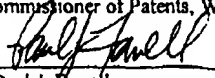
IN THE TITLE:

Please change the title to: "SEMICONDUCTOR DEVICE UTILIZING ALIGNMENT MARKS FOR GLOBALLY ALIGNING THE FRONT AND BACK SIDES OF A SEMICONDUCTOR SUBSTRATE".

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8 (a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner of Patents, Washington, DC 20231

Dated: August 23, 2002


Paul J. Farrell

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